

Form PTO-1449 (modified)

Atty. Docket No.

ALTR:024

Serial No.

10/736,167

List of Patents and Publications for Applicant's

Applicant:

KRISHNAPPA ET AL.

Filing Date:

12/15/03

Group:

2819

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

U.S. Patent Documents

See Page 1

Foreign Patent Documents

See Page

Other Art

See Page 1

U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
DC	A1	6,507,943	1/14/03	Kelem			9/26/00

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
DC	C1	Leventis et al., "Cyclone: A Low-Cost, High-Performance FPGA", IEEE Custom Integrated Circuits Conference, Sept. 21-24, 2003, pps. 49-52.
DC	C2	"Cyclone Device Handbook, Vol. 1, Ch. 13, "Configuring Cyclone FPGAs", Altera Corporation, July 2003, pps. 1-56.
DC	C3	International Search Report, PCT/US2004/042064, ALTR:024PCT, 13 pgs. 12-15-04

Examiner:

Date Considered:

12/12/05

EXAMINER: initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

